

**Notice of References Cited**

Application/Control No.

09/437,590

Applicant(s)/Patent Under

Reexamination

CANDELORE, BRANT L.

Examiner

John M Winter

Art Unit

3621

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 2003330897 A	11-2003	JAPAN	Padmanabhan	G06F015/00
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Energy-Efficient Data Scrambling on Memory Processor Interfaces, Benini, August 2003p
	V	
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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